

FIG. 1
BACKGROUND ART

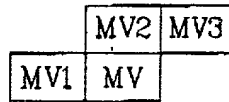


FIG. 2
BACKGROUND ART

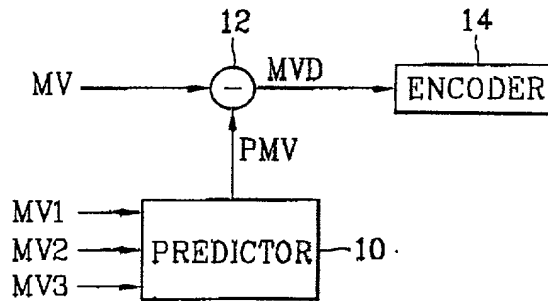


FIG. 3A
BACKGROUND ART

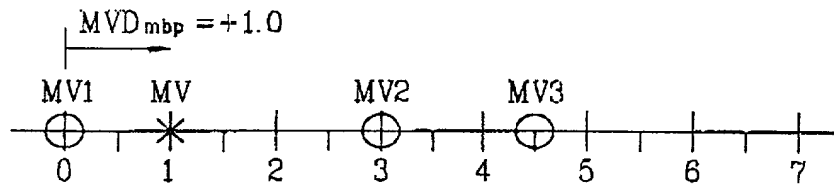


FIG. 3B
BACKGROUND ART

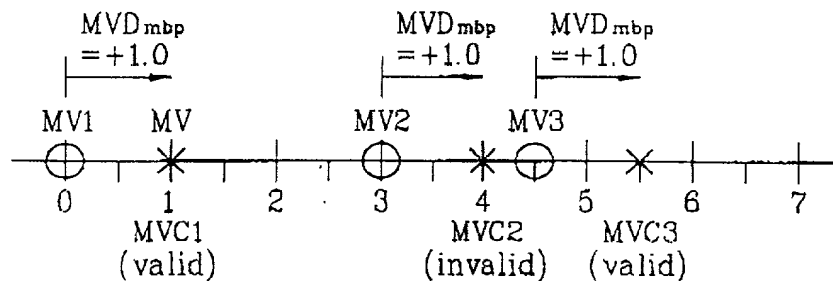


FIG. 4

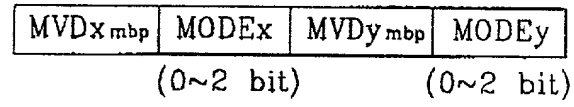


FIG. 5

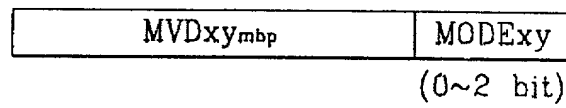


FIG. 6

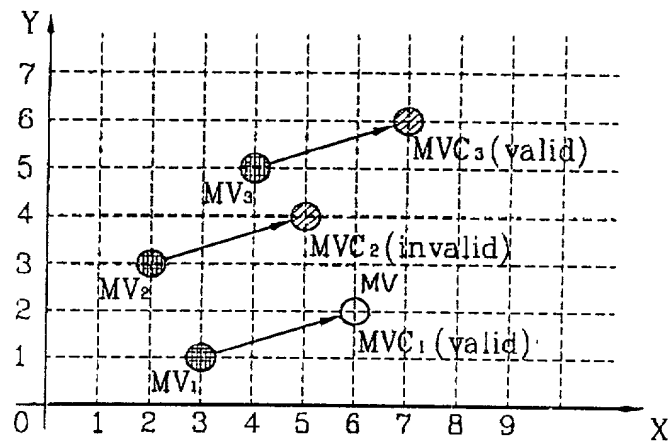


FIG. 7

No. of valid MVCi	True motion vector among valid MVCi			No. of bits	Code
1	Unique			0	x
2	MVC1	MVC1	MVC2	1	"0"
	MVC2	MVC3	MVC3	1	"1"
3		MVC1		1	"0"
		MVC2		2	"10"
		MVC3		2	"11"

FIG. 8

Syntax(Code)	Meaning
"1"	MVDx=0 and MVDy=0
"00"+MVDx+MVDy	MVDx≠0 and MVDy≠0
"010"+MVDx	MVDx≠0 and MVDy=0
"011"+MVDy	MVDx=0 and MVDy≠0

FIG. 9

MVDx _{mbp} + MVDy _{mbp}	MODEx + MODEy
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(0~4 bit)